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## DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

### SCALABLE SELF-ALIGNED DUAL FLOATING GATE MEMORY CELL ARRAY AND METHODS OF FORMING THE ARRAY

which (check)  is attached hereto.  
 and is amended by the Preliminary Amendment attached hereto.  
 was filed on August 8, 2001 as Application Serial No. 09/925,102  
 and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of first joint inventor: Jack H. Yuan  
 Inventor's Signature: Jack H. Yuan Date: 10/18/01  
 Residence: Cupertino, California 95014  
 Post Office Address: 10325 Tula Lane Citizenship: U.S.A.  
 Cupertino, California 95014

Full name of second joint inventor: Eliyahou Harari  
 Inventor's Signature: Eliyahou Harari Date: 10, 20, 01  
 Residence: Los Gatos, California 95030  
 Post Office Address: 104 Auzerais Court Citizenship: U.S.A.  
 Los Gatos, California 95030

Full name of third joint inventor: Yupin K. Fong  
Inventor's Signature: *Yupin Fong* Date: 10/22/01  
Residence: Fremont, California 94539  
Post Office Address: 2100 Springwater Drive  
Fremont, California 94539 Citizenship: U.S.A.

Full name of fourth joint inventor: George Samachisa  
Inventor's Signature: *George* Date: 10-18-01  
Residence: San Jose, California 95120  
Post Office Address: 6858 Castlerock Drive  
San Jose, California 95120 Citizenship: U.S.A.

#807698

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jack H. Yuan, Eliyahou Harari, Yupin K. Fong and George Samachisa  
Assignee: SanDisk Corporation  
Title: Scalable Self-Aligned Dual Floating Gate Memory Cell Array and  
Methods of Forming the Array  
Serial No.: 09/925,102 Filing Date: August 8, 2001  
Docket No.: 11587 M-11822 US Group Art Unit: 2185

San Francisco, California

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE  
OF ENTIRE INTEREST**

Sir:

The above-identified assignee hereby revokes all powers of attorney previously given and appoints hereby the following attorney(s) and/or agent(s) to prosecute the above-identified application and to transact all business in the United States Patent and Trademark Office in connection therewith:



**27869**

PATENT TRADEMARK OFFICE

Please address all correspondence and telephone calls regarding this application to:

Gerald P. Parsons  
SKJERVEN MORRILL MacPHERSON LLP  
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San Francisco, California 94111  
Telephone: (415) 216-6000  
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**ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)**

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title and interest in the above-identified patent application by virtue of a chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee as shown below:

LAW OFFICES OF  
SKJERVEN MORRILL  
MacPHERSON LLP

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A copy of the executed Assignment is attached.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

10/16/2001

Date



Steven S. Baik,  
Senior Intellectual Property Counsel

LAW OFFICES OF  
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